



Issued Date: Apr. 08, 2008 Model No.: N156B6-L02

Tentative

TFT LCD Tentative Specification

MODEL NO.: N156B6-L02

(Without Converter)

Customer :	
Approved by :	-
Note:	

記錄	工作	審核	角色	投票
2008-05-02 14:12:22 CST	PMMD Director	cs_lee(李志聖 /56510/44926)	Director	Accept





Issued Date: Apr. 08, 2008 Model No.: N156B6-L02

Tentative

- CONTENTS -

REVISION HISTORY	 3
1. GENERAL DESCRIPTION 1.1 OVERVIEW 1.2 FEATURES 1.3 APPLICATION 1.4 GENERAL SPECIFICATIONS 1.5 MECHANICAL SPECIFICATIONS	4
2. ABSOLUTE MAXIMUM RATINGS 2.1 ABSOLUTE RATINGS OF ENVIRONMENT 2.2 ELECTRICAL ABSOLUTE RATINGS 2.2.1 TFT LCD MODULE 2.2.2 BACKLIGHT UNIT	5
3. ELECTRICAL CHARACTERISTICS 3.1 TFT LCD MODULE 3.2 BACKLIGHT UNIT	7
4. BLOCK DIAGRAM 4.1 TFT LCD MODULE 4.2 BACKLIGHT UNIT	10
5. INPUT TERMINAL PIN ASSIGNMENT 5.1 TFT LCD MODULE 5.2 BACKLIGHT UNIT 5.3 TIMING DIAGRAM OF LVDS INPUT SIGNAL 5.4 COLOR DATA INPUT ASSIGNMENT 5.5 EDID DATA STRUCTURE 5.6 EDID SIGNAL SPECIFICATION	11
6. INTERFACE TIMING 6.1 INPUT SIGNAL TIMING SPECIFICATIONS 6.2 POWER ON/OFF SEQUENCE	 17
7. OPTICAL CHARACTERISTICS 7.1 TEST CONDITIONS 7.2 OPTICAL SPECIFICATIONS	 19
8. PRECAUTIONS 8.1 HANDLING PRECAUTIONS 8.2 STORAGE PRECAUTIONS 8.3 OPERATION PRECAUTIONS	 23
9. PACKING 9.1 CARTON 9.2 PALLET	 24
10. DEFINITION OF LABELS 10.1 CMO MODULE LABEL 10.2 CARTON LABEL	 26





Issued Date: Apr. 08, 2008 Model No.: N156B6-L02

Tentative

REVISION HISTORY

Version	Date	Page (New)	Section	Description
	Date Apr.08, 2008	Page (New)	All	Tentative specification first issued.



Issued Date: Apr. 08, 2008 Model No.: N156B6-L02

Tentative

1. GENERAL DESCRIPTION

1.1 OVERVIEW

N156B6-L02 is a 15.6" TFT Liquid Crystal Display module with LED Backlight unit and 30 pins LVDS interface. This module supports 1366 x 768 Wide-XGA mode and can display 262,144 colors. The optimum viewing angle is at 6 o'clock direction. The converter module for Backlight is not built in.

1.2 FEATURES

- Thin and light weight
- WXGA (1366 x 768 pixels) resolution
- 3.3V LVDS (Low Voltage Differential Signaling) interface with 1 pixel/clock

1.3 APPLICATION

- TFT LCD Notebook

1.4 GENERAL SPECIFICATIONS

Item	Specification	Unit	Note
Active Area	344.232 (H) x 193.536 (V) (15.6" diagonal)	mm	(1)
Bezel Opening Area	348.43 (H) x 197.74 (V)	mm	(1)
Driver Element	a-si TFT active matrix	-	-
Pixel Number	1366 x R.G.B. x 768	pixel	-
Pixel Pitch	0.252 (H) x 0.252 (V)	mm	-
Pixel Arrangement	RGB vertical stripe	-	-
Display Colors	262,144	color	-
Transmissive Mode	Normally white	-	-
Surface Treatment	Hard coating (3H), Glare	-	-

1.5 MECHANICAL SPECIFICATIONS

It	Item		Тур.	Max.	Unit	Note
	Horizontal(H)	358.8	359.3	359.8	mm	
Module Size	Vertical(V)	209	209.5	210	mm	(1)
	Thickness(T)	-	5.9	6.2	mm	
Weight			490	505	g	-

Note (1) Please refer to the attached drawings for more information of front and back outline dimensions.



Issued Date: Apr. 08, 2008 Model No.: N156B6-L02

Tentative

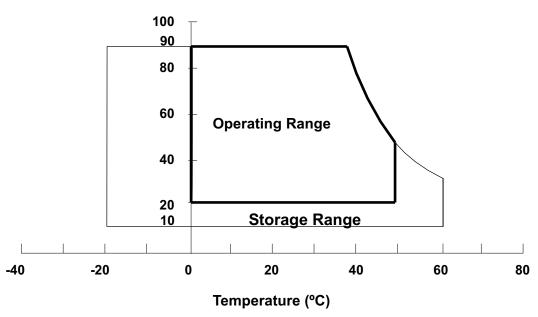
2. ABSOLUTE MAXIMUM RATINGS

2.1 ABSOLUTE RATINGS OF ENVIRONMENT

Item	Symbol	Va	Unit	Note		
Item	Symbol	Min.	Max.	Offic	NOLE	
Storage Temperature	T _{ST}	-20	+60	°C	(1)	
Operating Ambient Temperature	T _{OP}	0	+50	°C	(1), (2)	
Shock (Non-Operating)	S _{NOP}	-	200/2	G/ms	(3), (5)	
Vibration (Non-Operating)	V_{NOP}	-	1.5	G	(4), (5)	

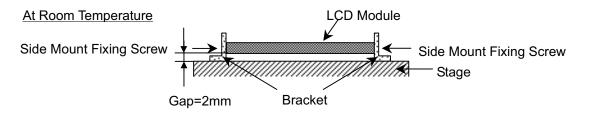
- Note (1) (a) 90 %RH Max. ($Ta \le 40 \, ^{\circ}C$).
 - (b) Wet-bulb temperature should be 39 °C Max. (Ta > 40 °C).
 - (c) No condensation.
- Note (2) The temperature of panel surface should be 0 °C min. and 50 °C max.

Relative Humidity (%RH)



- Note (3) 1 time for ± X, ± Y, ± Z. for Condition (200G / 2ms) is half Sine Wave,.
- Note (4) 10~500 Hz, 0.5hr/cycle 1cycle for X,Y,Z
- Note (5) At testing Vibration and Shock, the fixture in holding the module has to be hard and rigid enough so that the module would not be twisted or bent by the fixture.

The fixing condition is shown as below:







Issued Date: Apr. 08, 2008 Model No.: N156B6-L02

Tentative

2.2 ELECTRICAL ABSOLUTE RATINGS

2.2.1 TFT LCD MODULE

Item	Symbol	Va	lue	Unit	Note	
item	Symbol	Min.	Max.	Offic	Note	
Power Supply Voltage	Vcc	-0.3	+4.0	V	(1)	
Logic Input Voltage	V_{IN}	-0.3	Vcc+0.3	V	(1)	

2.2.2 BACKLIGHT UNIT

Itom	Symbol	\ \	/alue	Unit	Note
Item	Symbol	Min.	Max.	Offic	Note
LED Light Bar Power Supply Voltage	V_L	0	TBD	V	(1), (2)
LED Light Bar Power Supply Current	ΙL	0	TBD	mA	(1), (2)

Note (1) Permanent damage to the device may occur if maximum values are exceeded. Function operation should be restricted to the conditions described under Normal Operating Conditions.

Note (2) Specified values are for LED (Refer to 3.2 for further information).





Issued Date: Apr. 08, 2008 Model No.: N156B6-L02

Tentative

3. ELECTRICAL CHARACTERISTICS

3.1 TFT LCD MODULE

Ta = 25 ± 2 °C

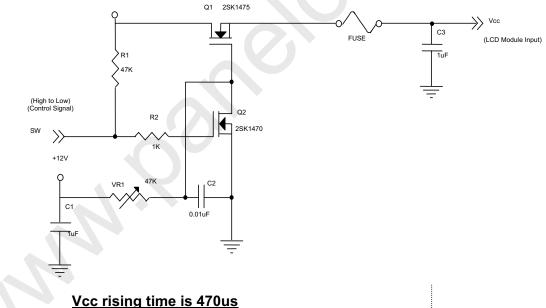
Parameter		Symbol		Value	Unit	Note	
Parameter	Symbol	Min.	Тур.	Max.	Offic	Note	
Power Supply Voltage		Vcc	3.0	3.3	3.6	V	-
Ripple Voltage		V_{RP}	-	-	100	mV	-
Rush Current		I _{RUSH}	-	•	1.5	Α	(2)
Initial Stage Current		I _{IS}	-	-	1.0	Α	(2)
Power Supply Current	White	_		(320)	TBD	mΑ	(3)a
Power Supply Current	Black	-		(380)	TBD	mA	(3)b
LVDS Differential Input High	Threshold	V _{TH(LVDS)}	-	-	+100	mV	(5), V _{CM} =1.2V
LVDS Differential Input Low Threshold		V _{TL(LVDS)}	-100	ı	-	mV	(5) V _{CM} =1.2V
LVDS Common Mode Voltage		V_{CM}	1.125	-	1.375	V	(5)
LVDS Differential Input Voltage		$ V_{ID} $	100	-	600	mV	(5)
Terminating Resistor	R _T	-	100	-	Ohm	-	
Power per EBL WG		P _{EBL}	-	TBD	-	W	(4)

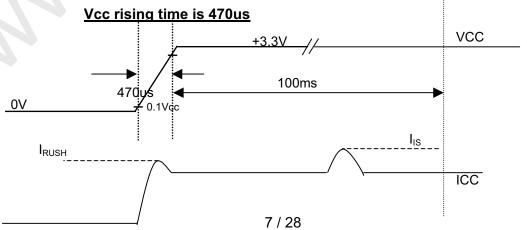
Note (1) The ambient temperature is $Ta = 25 \pm 2$ °C.

Note (2) I_{RUSH}: the maximum current when VCC is rising

 $\ensuremath{I_{\text{IS}}}\xspace$ the maximum current of the first 100ms after power-on

Measurement Conditions: Shown as the following figure. Test pattern: black.



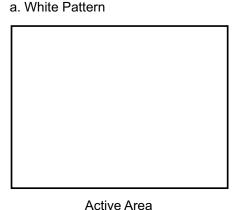




Issued Date: Apr. 08, 2008 Model No.: N156B6-L02

Tentative

Note (3) The specified power supply current is under the conditions at Vcc = 3.3 V, Ta = 25 \pm 2 °C, DC Current and f_v = 60 Hz, whereas a power dissipation check pattern below is displayed.

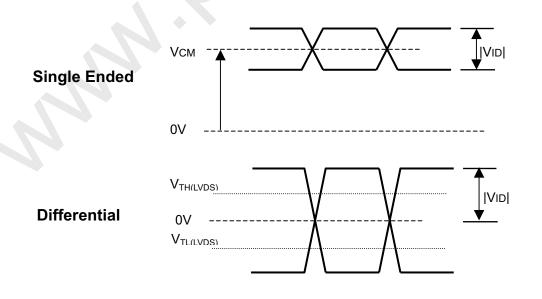


b. Black Pattern



Active Area

- Note (4) The specified power are the sum of LCD panel electronics input power and the converter input power. Test conditions are as follows.
 - (a) Vcc = 3.3 V, $Ta = 25 \pm 2 \, ^{\circ}C$, $f_v = 60 Hz$,
 - (b) The pattern used is a black and white 32 x 36 checkerboard, slide #100 from the VESA file "Flat Panel Display Monitor Setup Patterns", FPDMSU.ppt.
 - (c) Luminance: 60 nits.
 - (d) The converter used is provided from <u>Sumida</u>. Please contact them for detail information. CMO doesn't provide the converter in this product.
- Note (5) The parameters of LVDS signals are defined as the following figures.



Ta = 25 ± 2 °C





Doc No.:

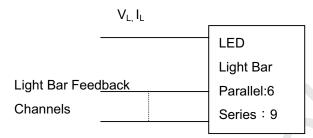
Issued Date: Apr. 08, 2008 Model No.: N156B6-L02

Tentative

3.2 BACKLIGHT UNIT

Downwater	Cymahal		Value	l lm!4	Mata	
Parameter	Symbol	Min.	Тур.	Max.	Unit	Note
LED Quantity			54		PCs	(1)
LED Light Bar Power Supply Voltage	V _L	26.1	27.9	29.7	V	(1) (2) (Duty 100%)
LED Light Bar Power Supply Current	Ι _L		120		mA	(1),(2) (Duty 100%)
Power Consumption	P_L		3.348		W	(3), (Duty 100%)
LED Life Time	L_{BL}	15000	-	-	Hrs	(4)

Note (1) LED light bar configuration is shown as below.



Note (2) For better LED light bar driving quality, it is recommended to utilize the adaptive boost converter with current balancing function to drive LED light-bar.

Note (3) $P_L = I_L \times V_L$

Note (4) The lifetime of LED is defined as the time when it continues to operate under the conditions at Ta = 25 \pm 2 °C and I_L = 20 mA(Per EA) until the brightness becomes \leq 50% of its original value.

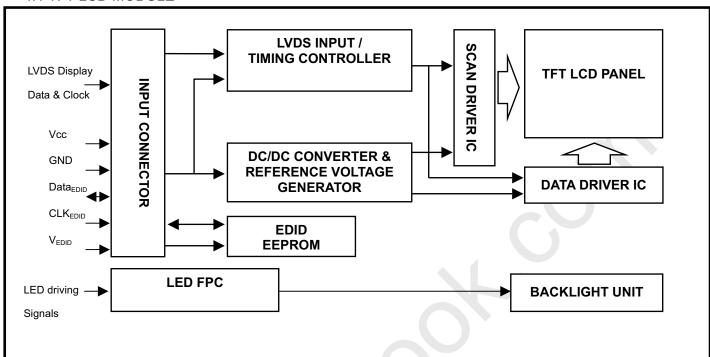


Issued Date: Apr. 08, 2008 Model No.: N156B6-L02

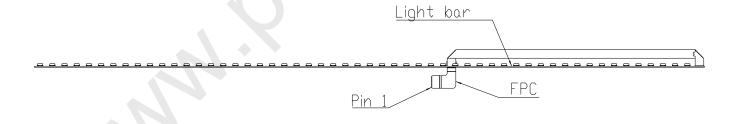
Tentative

4. BLOCK DIAGRAM

4.1 TFT LCD MODULE



4.2 BACKLIGHT UNIT





Issued Date: Apr. 08, 2008 Model No.: N156B6-L02

Tentative

5. INPUT TERMINAL PIN ASSIGNMENT

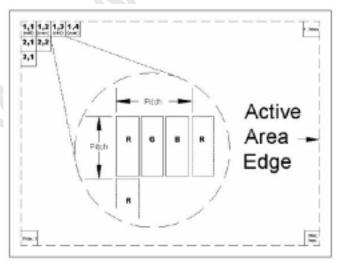
5.1 TFT LCD MODULE

Pin	Symbol	Description	Polarity	Remark
1	Vss	Ground		
2	Vcc	Power Supply +3.3 V (typical)		
3	Vcc	Power Supply +3.3 V (typical)		
4	V_{EDID}	DDC 3.3V Power		DDC 3.3V Power
5	NC	Non-Connection		
6	CLK _{EDID}	DDC Clock		DDC Clock
7	DATA _{EDID}	DDC Data		DDC Data
8	Rxin0-	LVDS Differential Data Input	Negative	R0~R5,G0
9	Rxin0+	LVDS Differential Data Input	Positive	
10	Vss	Ground		
11	Rxin1-	LVDS Differential Data Input	Negative	G1~G5, B0, B1
12	Rxin1+	LVDS Differential Data Input	Positive	
13	Vss	Ground		
14	Rxin2-	LVDS Differential Data Input	Negative	B2~B5, DE, Hsync, Vsync
15	Rxin2+	LVDS Differential Data Input	Positive	
16	Vss	Ground		
17	CLK-	LVDS Clock Data Input	Negative	LVDS Level Clock
18	CLK+	LVDS Clock Data Input	Positive	LVD3 Level Clock
19	Vss	Ground		
20	NC	Non-Connection		
21	NC	Non-Connection		
22	Vss	Ground		
23	NC	Non-Connection		
24	NC	Non-Connection		
25	Vss	Ground		
26	NC	Non-Connection		
27	NC	Non-Connection		
28	Vss	Ground		
29	NC	Non-Connection		
30	NC	Non-Connection		

Non-Connection Note (1) Connector Part No. JAE FI-XB30SL-HF10 OR EQUIVALENT

Note (2) User's connector Part No: JAE-FI-X30M or equivalent

Note (3) The first pixel is odd as shown in the following figure.





Issued Date: Apr. 08, 2008 Model No.: N156B6-L02

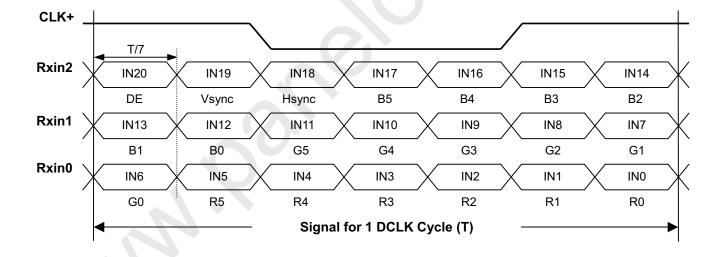
Tentative

5.2 Backlight FPC Pin Assignment

Pin	Symbol	Description
1	V _L	LED Light-bar Input Power
2	V _L	LED Light-bar Input Power
3	V _L	LED Light-bar Input Power
4	NC	No connection
5	CH1	Light-bar Feedback Channel 1
6	CH2	Light-bar Feedback Channel 2
7	CH3	Light-bar Feedback Channel 3
8	CH4	Light-bar Feedback Channel 4
9	CH5	Light-bar Feedback Channel 5
10	CH6	Light-bar Feedback Channel 6
11	CH7	Light-bar Feedback Channel 7
12	CH8	Light-bar Feedback Channel 8

Note (1) User's connector Part No: STARCONN 089H12-000000-G2-R or equivalent

5.3 TIMING DIAGRAM OF LVDS INPUT SIGNAL





Issued Date: Apr. 08, 2008 Model No.: N156B6-L02

Tentative

5.4 COLOR DATA INPUT ASSIGNMENT

The brightness of each primary color (red, green and blue) is based on the 6-bit gray scale data input for the color. The higher the binary input the brighter the color. The table below provides the assignment of color versus data input.

		Data Signal																	
Color				Re							een						ue		
		R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B5	B4	B3	B2	B1	B0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Basic	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
Colors	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	Ö	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Red(0)/Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(1)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
Gray	Red(2)	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
Scale	:	:	:	:	:	:	:	:	:	:			:		:	:	:	:	:
Of	:	:	:	:	:	:	:	:	:	:	:			:	:	:	:	:	:
Red	Red(61)	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red(62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green(0)/Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(1)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
Gray	Green(2)	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
Scale	\ `:´	:	:	:	:	:	:			:	:	:	:	:	:	:	:	:	:
Of	:	:	:	:	:	:				:	:	:	:	:	:	:	:	:	:
Green	Green(61)	0	0	0	0	0	0	1	1	1	1	0	1	0	0	0	0	0	0
	Green(62)	0	0	0	0 <	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	Green(63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Blue(0)/Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
Gray	Blue(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
Scale	:	:			:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
Of	:	:		:/	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
Blue	Blue(61)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	0	1
	Blue(62)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	Blue(63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
					_	_		-	-	_									

Note (1) 0: Low Level Voltage, 1: High Level Voltage



Issued Date: Apr. 08, 2008 Model No.: N156B6-L02

Tentative

5.5 EDID DATA STRUCTURE

The EDID (Extended Display Identification Data) data formats are to support displays as defined in the VESA Plug & Display and FPDI standards.

0 0 1 1 2 2 3 3	Field Name and Comments Header Header	Value(hex)	Value(binary) 00000000
1 1 2 2 3 3	Header		00000000
2 2 3 3			0000000
3 3		FF	11111111
	Header	FF	11111111
4 4	Header	FF	11111111
	Header	FF	11111111
5 5	Header	FF	11111111
6 6	Header	FF	11111111
7 7	Header	00	00000000
8 8	EISA ID manufacturer name ("CMO")	0D	00001101
	EISA ID manufacturer name (Compressed ASCII)	AF	10101111
	ID product code (N156B6-L02)	66	01100110
	ID product code (hex LSB first; N156B6-L02)	15	00010101
	ID S/N (fixed "0")	00	00000000
	ID S/N (fixed "0")	00	00000000
	ID S/N (fixed "0")	00	00000000
	ID S/N (fixed "0")	00	00000000
	Week of manufacture (fixed "00H")	28	00101000
	Year of manufacture (fixed "00H")	11	00010001
	EDID structure version # ("1")	01	0000001
	EDID revision # ("3")	03	00000011
	Video I/P definition ("digital")	80	10000000
	Max H image size ("35cm")	23	00100011
	Max V image size ("19cm")	13	00010011
	Display Gamma (Gamma = "2.2")	78	01111000
24 18	Feature support ("Active off, RGB Color")	0A	00001010
	Red/Green (Rx1, Rx0, Ry1, Ry0, Gx1, Gx0, Gy1, Gy0)	07	00000111
	Blue/White (Bx1, Bx0, By1, By0, Wx1, Wx0, Wy1, Wy0)	F5	11110101
	Red-x (Rx = "0.602")	9A	10011010
	Red-y (Ry = "0.340")	57	01010111
	Green-x (Gx = "0.306")	4E	01001110
	Green-y (Gy = "0.530")	87	10000111
	Blue-x (Bx = "0.151")	26	00100110
	Blue-y (By = "0.120")	1E	00011110
	White-x (Wx = "0.313")	50	01010000
	White-y (Wy = "0.329")	54	01010100
35 23	Established timings 1	00	00000000
	Established timings 2	00	00000000
	Manufacturer's reserved timings	00	00000000
	Standard timing ID # 1	01	0000001
	Standard timing ID # 1	01	0000001





Issued Date: Apr. 08, 2008 Model No.: N156B6-L02

Tentative

	ОРТ	OELECTRONICS CORP.		rentative
40	28	Standard timing ID # 2	01	00000001
41	29	Standard timing ID # 2	01	00000001
42	2A	Standard timing ID # 3	01	00000001
43	2B	Standard timing ID # 3	01	00000001
44	2C	Standard timing ID # 4	01	00000001
45	2D	Standard timing ID # 4	01	00000001
46	2E	Standard timing ID # 5	01	00000001
47	2F	Standard timing ID # 5	01	00000001
48	30	Standard timing ID # 6	01	0000001
49	31	Standard timing ID # 6	01	0000001
50	32	Standard timing ID # 7	01	0000001
51	33	Standard timing ID # 7	01	0000001
52	34	Standard timing ID # 8	01	0000001
53	35	Standard timing ID # 8	01	0000001
54		Detailed timing 15 " 6 Detailed timing description # 1 Pixel clock ("75.5MHz", According to VESA CVT Rev1.1)	7E	01111110
55		# 1 Pixel clock (hex LSB first)	1D	00011101
56		# 1 H active ("1366")	56	01010110
57		# 1 H blank ("194")	C2	11000010
58		# 1 H active : H blank ("1366 :194")	50	01010000
59		# 1 V active ("768")	00	00000000
60		# 1 V blank ("38")	26	00100110
61		# 1 V active : V blank ("768 :38")	30	00110000
62		# 1 H sync offset ("31")	1F	00011111
63		# 1 H sync pulse width ("65")	41	01000001
64		# 1 V sync offset : V sync pulse width ("4 : 12")	4C	01001100
65	41	# 1 H sync offset : H sync pulse width : V sync offset : V sync width ("31: 65 : 4 : 12")	00	00000000
66	42	# 1 H image size ("344 mm")	58	01011000
67		# 1 V image size ("193 mm")	C1	11000001
68		# 1 H image size : V image size ("344 : 193")	10	00010000
69		# 1 H boarder ("0")	00	00000000
70		# 1 V boarder ("0")	00	00000000
71		# 1 Non-interlaced, Normal, no stereo, Separate sync, H/V pol Negatives	18	00011000
72	48	Detailed timing description # 2	00	00000000
73	49	# 2 Flag	00	00000000
74	4A	# 2 Reserved	00	00000000
75	4B	# 2 FE (hex) defines ASCII string (Model Name "N156B6-L02", ASCII)	FE	11111110
76		# 2 Flag	00	00000000
77		# 2 1st character of name ("N")	4E	01001110
78		# 2 2nd character of name ("1")	31	00110001
79		# 2 3rd character of name ("5")	35	00110101
80		# 2 4th character of name ("6")	36	00110110
81		# 2 5th character of name ("B")	42	01000010
82		# 2 6th character of name ("6")	36	00110110
83		# 2 7th character of name ("-")	2D	00101101





Issued Date: Apr. 08, 2008 Model No.: N156B6-L02

Tentative

	Ј ОРТ	OELECTRONICS CORP.		Tentative
84	54	# 2 8th character of name ("L")	4C	01001100
85		# 2 9th character of name ("0")	30	00110000
86	56	# 2 9th character of name ("2")	32	00110010
87	57	# 2 New line character indicates end of ASCII string	0A	00001010
88	58	# 2 Padding with "Blank" character	20	00100000
89	59	# 2 Padding with "Blank" character	20	00100000
90	5A	Detailed timing description # 3	00	00000000
91	5B	# 3 Flag	00	00000000
92	5C	# 3 Reserved	00	00000000
93	5D	# 3 FE (hex) defines ASCII string (Vendor "CMO", ASCII)	FE	11111110
94	5E	# 3 Flag	00	00000000
95	5F	# 3 1st character of string ("C")	43	01000011
96	60	# 3 2nd character of string ("M")	4D	01001101
97	61	# 3 3rd character of string ("O")	4F	01001111
98	62	# 3 New line character indicates end of ASCII string	0A	00001010
99	63	# 3 Padding with "Blank" character	20	00100000
100	64	# 3 Padding with "Blank" character	20	00100000
101	65	# 3 Padding with "Blank" character	20	00100000
102	66	# 3 Padding with "Blank" character	20	00100000
103	67	# 3 Padding with "Blank" character	20	00100000
104	68	# 3 Padding with "Blank" character	20	00100000
105	69	# 3 Padding with "Blank" character	20	00100000
106	6A	# 3 Padding with "Blank" character	20	00100000
107	6B	# 3 Padding with "Blank" character	20	00100000
108	6C	Detailed timing description # 4	00	0000000
109	6D	# 4 Flag	00	0000000
110	6E	# 4 Reserved	00	0000000
111	6F	# 4 FE (hex) defines ASCII string (Model Name"N156B6-L02", ASCII)	FE	11111110
112	70	# 4 Flag	00	0000000
113	71	# 4 1st character of name ("N")	4E	01001110
114	72	# 4 2nd character of name ("1")	31	00110001
115	73	# 4 3rd character of name ("5")	35	00110101
116	74	# 4 4th character of name ("6")	36	00110110
117	75	# 4 5th character of name ("B")	42	01000010
118	76	# 4 6th character of name ("6")	36	00110110
119	77	# 4 7th character of name ("-")	2D	00101101
120	78	# 4 8th character of name ("L")	4C	01001100
121	79	# 4 9th character of name ("0")	30	00110000
122	7A	# 4 9th character of name ("2")	32	00110010
123	7B	# 4 New line character indicates end of ASCII string	0A	00001010
124	7C	# 4 Padding with "Blank" character	20	00100000
125	7D	# 4 Padding with "Blank" character	20	00100000
126	7E	Extension flag	00	0000000
127	7F	Checksum	49	01001001





Global LCD Panel Exchange Center

Doc No.:

Issued Date: Apr. 08, 2008 Model No.: N156B6-L02

Tentative

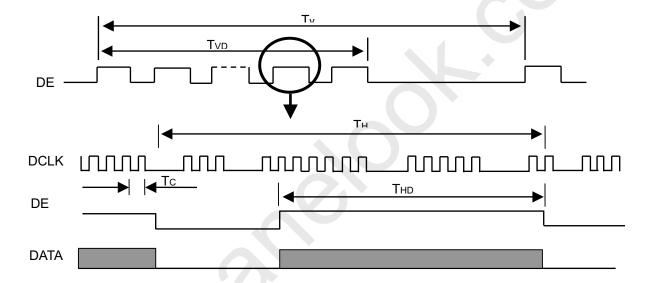
6. INTERFACE TIMING

6.1 INPUT SIGNAL TIMING SPECIFICATIONS

The input signal timing specifications are shown as the following table and timing diagram.

Signal	Item	Symbol	Min.	Тур.	Max.	Unit	Note
DCLK	Frequency	1/Tc	50	(76)	(85)	MHz	(2)
	Vertical Total Time	TV	(778)	(806)	(888)	TH	-
	Vertical Active Display Period	TVD	768	768	768	TH	-
DE	Vertical Active Blanking Period	TVB	TV-TVD	(38)	TV-TVD	H	
DE	Horizontal Total Time	TH	(1446)	(1560)	(1936)	Tc	(2)
	Horizontal Active Display Period	THD	1366	1366	1366	Tc	(2)
	Horizontal Active Blanking Period	THB	TH-THD	(194)	TH-THD	Tc	(2)

INPUT SIGNAL TIMING DIAGRAM



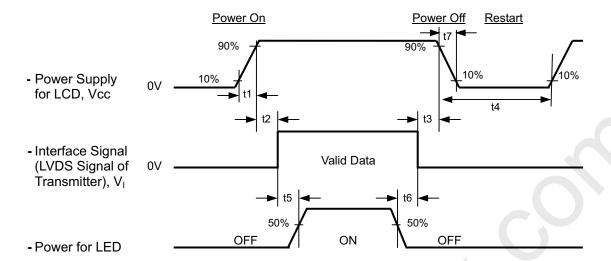




Issued Date: Apr. 08, 2008 Model No.: N156B6-L02

Tentative

6.2 POWER ON/OFF SEQUENCE



Timing Specifications:

0.5< t1 <= 10 msec

0 < t2 <= 50 msec

0 < t3 <= 50 msec

t4 >= 500 msec

t5 >= 200 msec

t6 >= 200 msec

- Note (1) Please follow the power on/off sequence described above. Otherwise, the LCD module might be damaged.
- Note (2) Please avoid floating state of interface signal at invalid period. When the interface signal is invalid, be sure to pull down the power supply of LCD Vcc to 0 V.
- Note (3) The Backlight converter power must be turned on after the power supply for the logic and the interface signal is valid. The Backlight converter power must be turned off before the power supply for the logic and the interface signal is invalid.
- Note (4) Sometimes some slight noise shows when LCD is turned off (even backlight is already off). To avoid this phenomenon, we suggest that the Vcc falling time is better to follow $5ms \le t7 \le 300$ ms.





Issued Date: Apr. 08, 2008 Model No.: N156B6-L02

Tentative

7. OPTICAL CHARACTERISTICS

7.1 TEST CONDITIONS

Item	Symbol	Value	Unit			
Ambient Temperature	Та	25±2	°C			
Ambient Humidity	На	50±10	%RH			
Supply Voltage	V_{CC}	3.3	V			
Input Signal	According to typical value in "3. ELECTRICAL CHARACTERISTICS					
Converter Current	IL	(120)	mA			

The measurement methods of optical characteristics are shown in Section 7.2. The following items should be measured under the test conditions described in Section 7.1 and stable environment shown in Note (5).

7.2 OPTICAL SPECIFICATIONS

Item		Symbol	Condition	Min.	Тур.	Max.	Unit	Note	
Contrast Ratio		CR		350	500		-	(2), (5)	
Pagnanaa Tima	B		T _R		3	8	ms	(3)	
Response Time	,	T _F		1	7	12	ms	(3)	
Average Lumina	ance of White	Lave		175	220	ı	cd/m ²	(4), (6)	
	Red	Rx			TBD		ı		
	Reu	Ry	$\theta_{x}=0^{\circ}, \ \theta_{Y}=0^{\circ}$		TBD		ı		
	Green	Gx	Viewing Normal Angle	TYP. -0.05	TBD	TYP. +0.05	-	(1)	
Color		Gy			TBD		-		
Chromaticity	Blue	Bx			TBD		-		
		Ву			TBD		-		
	White	Wx			0.313		-		
		Wy			0.329		ı		
	Harizantal	θ_x +		40	45	-			
Viewing Angle	Horizontal	θ_{x} -	OD: 40	40	45	-	Dog	(1) (5)	
	Vartical	θ_{Y} +	CR≥10	15	20	-	Deg.	(1),(5)	
	Vertical	θ _Y -		40	45	-			
White Variation	of 5 Points	δW_{5p}	$\theta_x = 0^\circ, \ \theta_Y = 0^\circ$	75	85	-	%	(5),(6)	



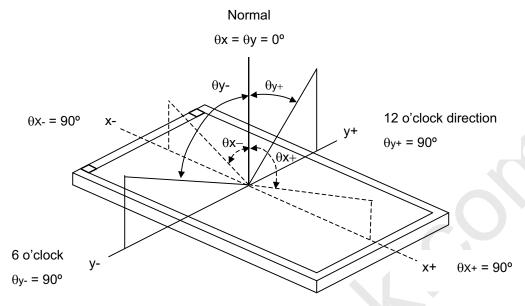


Global LCD Panel Exchange Center

Doc No.: Issued Date: Apr. 08, 2008 Model No.: N156B6-L02

Tentative

Note (1) Definition of Viewing Angle (θx , θy):



Note (2) Definition of Contrast Ratio (CR):

The contrast ratio can be calculated by the following expression.

Contrast Ratio (CR) = L63 / L0

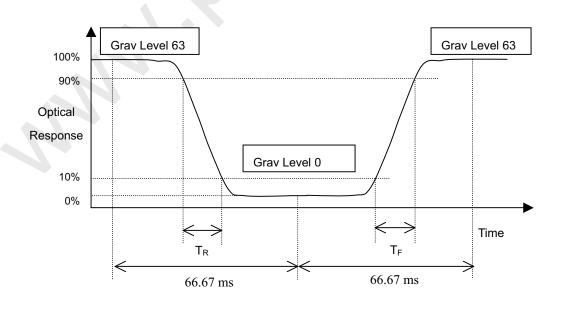
L63: Luminance of gray level 63

L 0: Luminance of gray level 0

CR = CR(1)

CR (X) is corresponding to the Contrast Ratio of the point X at Figure in Note (6).

Note (3) Definition of Response Time (T_R, T_F):







Issued Date: Apr. 08, 2008 Model No.: N156B6-L02

Tentative

Note (4) Definition of Average Luminance of White (L_{AVE}) :

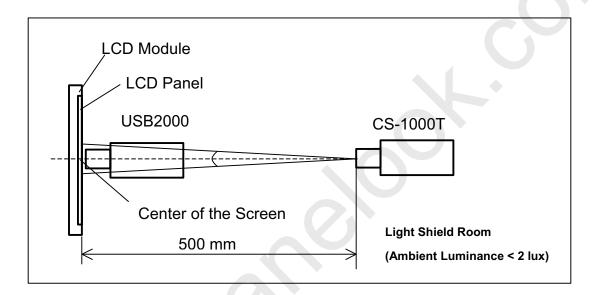
Measure the luminance of gray level 63 at 5 points

$$L_{AVE} = [L (1) + L (2) + L (3) + L (4) + L (5)] / 5$$

L (x) is corresponding to the luminance of the point X at Figure in Note (6)

Note (5) Measurement Setup:

The LCD module should be stabilized at given temperature for 20 minutes to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting Backlight for 20 minutes in a windless room.





Global LCD Panel Exchange Center

Doc No.:

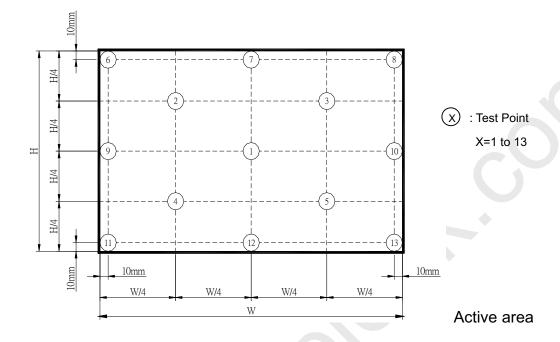
Issued Date: Apr. 08, 2008 Model No.: N156B6-L02

Tentative

Note (6) Definition of White Variation (δW):

Measure the luminance of gray level 63 at 5 points

 $\delta W_{5p} = \text{Minimum} \left[\text{L} \left(1 \right) + \text{L} \left(2 \right) + \text{L} \left(3 \right) + \text{L} \left(4 \right) + \text{L} \left(5 \right) \right] / \\ \text{Maximum} \left[\text{L} \left(1 \right) + \text{L} \left(2 \right) + \text{L} \left(3 \right) + \text{L} \left(4 \right) + \text{L} \left(5 \right) \right]$







Doc No.: Issued Date: Apr. 08, 2008 Model No.: N156B6-L02

Tentative

8. PRECAUTIONS

8.1 HANDLING PRECAUTIONS

- (1) The module should be assembled into the system firmly by using every mounting hole. Be careful not to twist or bend the module.
- (2) While assembling or installing modules, it can only be in the clean area. The dust and oil may cause electrical short or damage the polarizer.
- (3) Use fingerstalls or soft gloves in order to keep display clean during the incoming inspection and assembly process.
- (4) Do not press or scratch the surface harder than a HB pencil lead on the panel because the polarizer is very soft and easily scratched.
- (5) If the surface of the polarizer is dirty, please clean it by some absorbent cotton or soft cloth. Do not use Ketone type materials (ex. Acetone), Ethyl alcohol, Toluene, Ethyl acid or Methyl chloride. It might permanently damage the polarizer due to chemical reaction.
- (6) Wipe off water droplets or oil immediately. Staining and discoloration may occur if they left on panel for a long time.
- (7) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth. In case of contacting with hands, legs or clothes, it must be washed away thoroughly with soap.
- (8) Protect the module from static electricity, it may cause damage to the C-MOS Gate Array IC.
- (9) Do not disassemble the module.
- (10) Do not pull or fold the LED wire.
- (11) Pins of I/F connector should not be touched directly with bare hands.

8.2 STORAGE PRECAUTIONS

- (1) High temperature or humidity may reduce the performance of module. Please store LCD module within the specified storage conditions.
- (2) It is dangerous that moisture come into or contacted the LCD module, because the moisture may damage LCD module when it is operating.
- (3) It may reduce the display quality if the ambient temperature is lower than 10 °C. For example, the response time will become slowly, and the starting voltage of LED will be higher than the room temperature.

8.3 OPERATION PRECAUTIONS

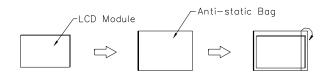
- (1) Do not pull the I/F connector in or out while the module is operating.
- (2) Always follow the correct power on/off sequence when LCD module is connecting and operating. This can prevent the CMOS LSI chips from damage during latch-up.
- (3) The startup voltage of Backlight is approximately 1000 Volts. It may cause electrical shock while assembling with converter. Do not disassemble the module or insert anything into the Backlight unit.



Issued Date: Apr. 08, 2008 Model No.: N156B6-L02

Tentative

9. PACKING9.1 CARTON



Box Dimensions: 489(L)*382(W)*330(H) Weight: Approx. 12.83kg(20 module .per. 1 box)

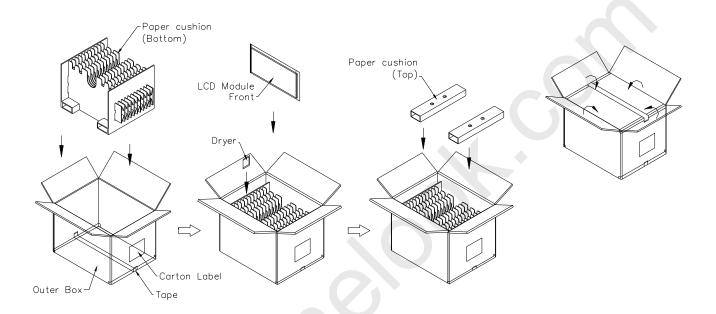


Figure. 9-1 Packing method



Issued Date: Apr. 08, 2008 Model No.: N156B6-L02

Tentative

9.2 PALLET

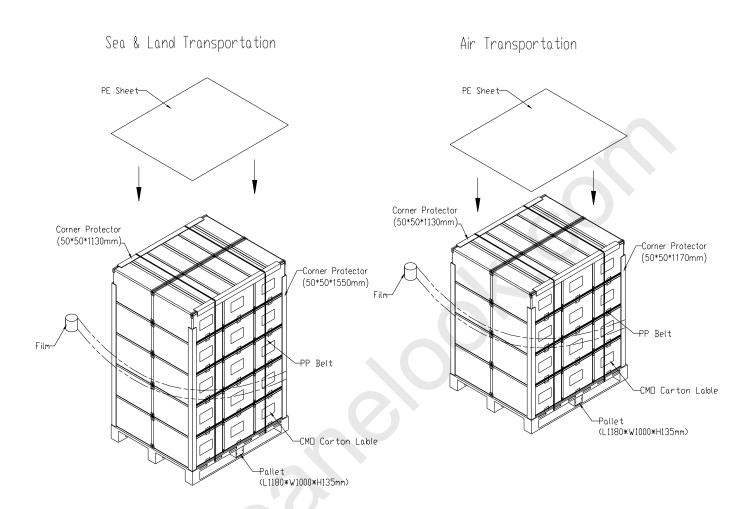


Figure. 9-2 Packing method



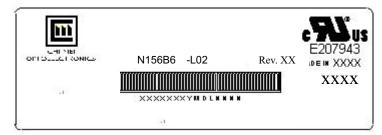
Issued Date: Apr. 08, 2008 Model No.: N156B6-L02

Tentative

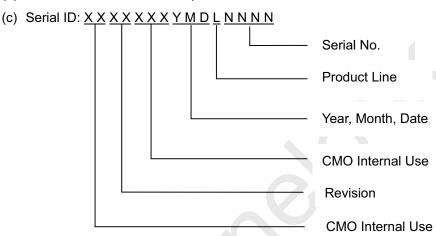
10. DEFINITION OF LABELS

10.1 CMO MODULE LABEL

The barcode nameplate is pasted on each module as illustration, and its definitions are as following explanation.



- (a) Model Name: N156B6 L02
- (b) Revision: Rev. XX, for example: C1, C2 ...etc.



Serial ID includes the information as beld Figure. 9-3 Packing method

(a) Manufactured Date: Year: 1~9, for 2001~2009

Month: 1~9, A~C, for Jan. ~ Dec.

Day: 1~9, A~Y, for 1st to 31st, exclude I, O and U

- (b) Revision Code: cover all the change
- (c) Serial No.: Manufacturing sequence of product
- (d) Product Line: 1 -> Line1, 2 -> Line 2, ...etc.





Doc No.: Issued Date: Apr. 08, 2008 Model No.: N156B6-L02

Tentative

10.2 CARTON LABEL

CHI MEI OPTOELECTRONICS		
PO.NO		
Part ID.		
Model Name		
Carton ID.	Quanti	ties
	Made in XXXX	GP RoHS

